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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 09/886,980 06/25/2001 PAT 6,656,800

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** FOREIGN APPLICATIONS *****

JAPAN 2000-191589 06/26/2000

KR

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 12/17/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiners Signature Initials	JAPAN	12	15	3

ADDRESS

22850
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TITLE

Method of manufacturing semiconductor device including process for implanting impurities into substrate via
 MOS transistor gate electrode and gate insulation film

FILING FEE RECEIVED 750	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)
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